



REVALUATION RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER  
EXAM

COLLEGE : GOA COLLEGE OF ENGINEERING

| SEAT No                         | P R No    | GENDER | Attempts | NAME OF CANDIDATE           |            |           |           |             |           |      |             |         |   |  |
|---------------------------------|-----------|--------|----------|-----------------------------|------------|-----------|-----------|-------------|-----------|------|-------------|---------|---|--|
|                                 |           |        |          | PAPER DESCRIPTION           | THEORY     | SESSIONAL | TERM WORK | TOTAL       | PRACTICAL | ORAL | TOTAL       | REMARKS |   |  |
| 203                             | 201610774 | M      | 1        | ABHISHEK BARGE              |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 56         | 10        | 22        | 88 P        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 50         | 15        |           | 65 P        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 55         | 12        |           | 67 P        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 63         | 12        |           | 75 P        | 13 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 65         | 17        |           | 82 P        | 24 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 51         | 15        |           | 66 P        |           | 18 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 498         |         | P |  |
| 204                             | 201610652 | M      | 1        | AIGAL ABHAY CHANDRAKANT     |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 43         | 11        | 22        | 76 P        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 40         | 14        |           | 54 P        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 42         | 14        |           | 56 P        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 59         | 11        |           | 70 P        | 10 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 62         | 21        |           | 83 P        | 20 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 29         | 12        |           | 41 F        |           | 17 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 427         |         | F |  |
| 205                             | 201704559 | F      | 1        | AKALWADI ASHVINI SATYAPPA   |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 26         | 10        | 22        | 58 F        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 17         | 15        |           | 32 F        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 24         | 15        |           | 39 F        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 55         | 15        |           | 70 P        | 3 F       |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 52         | 15        |           | 67 P        | 16 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 38         | \$2 10    |           | 48 P        | \$2       | 13 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 346         | \$2     | F |  |
| 207                             | 201610536 | F      | 1        | ATHALYE JANHAVI SHRIRAM     |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 67         | 14        | 23        | 104 P       |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 40         | 21        |           | 61 P        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 25 #10 \$5 | 14        |           | 39 P#10 \$5 |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 65         | 18        |           | 83 P        | 19 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 70         | 21        |           | 91 P        | 20 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 41         | 17        |           | 58 P        |           | 19 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 494 #10 \$5 |         | P |  |
| 213                             | 201610548 | M      | 1        | CHODANKAR NIHAL DEVIDAS     |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 40         | 11        | 20        | 71 P        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 35         | \$5 20    |           | 55 P        | \$5       |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 44         | 19        |           | 63 P        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 43         | 11        |           | 54 P        | 18 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 64         | 11        |           | 75 P        | 24 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 35         | \$5 18    |           | 53 P        | \$5       | 18 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 431         | \$10    | P |  |
| 218                             | 201610554 | M      | 1        | DESSAI DIGVIJAY MARUTI      |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 30         | 11        | 22        | 63 F        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 35         | \$5 16    |           | 51 P        | \$5       |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 40         | 13        |           | 53 P        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 51         | 6         |           | 57 P        | 11 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 52         | 10        |           | 62 P        | 18 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 51         | 10        |           | 61 P        |           | 21 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 397         | \$5     | F |  |
| 222                             | 201704558 | F      | 1        | EKAWADE PRANALEE JAIPRAKASH |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 40         | 10        | 21        | 71 P        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 22         | 12        |           | 34 F        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 21         | 18        |           | 39 F        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 42         | 10        |           | 52 P        | 8 F       |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 60         | 10        |           | 70 P        | 20 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 53         | 10        |           | 63 P        |           | 13 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 370         |         | F |  |
| 225                             | 201610566 | F      | 1        | GAONKAR DEEPTI DHARMA       |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 55         | 13        | 20        | 88 P        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 54         | 20        |           | 74 P        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 27         | 14        |           | 41 F        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 60         | 11        |           | 71 P        | 10 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 73         | 15        |           | 88 P        | 20 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 51         | 16        |           | 67 P        |           | 18 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 477         |         | F |  |
| 226                             | 201610691 | F      | 1        | GAONKAR MANJITA TANO        |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 49         | 11        | 25        | 85 P        |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 65         | 20        |           | 85 P        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 40         | 19        |           | 59 P        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 62         | 18        |           | 80 P        | 10 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 68         | 19        |           | 87 P        | 25 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 51         | 18        |           | 69 P        |           | 18 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 518         |         | P |  |
| 230                             | 201610574 | F      | 1        | KANEKAR SHWETA NANDKISHOR   |            |           |           |             |           |      |             |         |   |  |
| Digital Signal Processing       |           |        |          |                             | 66         | 20        | 25        | 111 P       |           |      |             |         |   |  |
| Transmission Lines and Antennas |           |        |          |                             | 78         | 19        |           | 97 P        |           |      |             |         |   |  |
| Control System Engineering      |           |        |          |                             | 70         | 24        |           | 94 P        |           |      |             |         |   |  |
| Embedded Systems                |           |        |          |                             | 89         | 25        |           | 114 P       | 25 P      |      |             |         |   |  |
| VLSI Design and Technology      |           |        |          |                             | 74         | 23        |           | 97 P        | 25 P      |      |             |         |   |  |
| Analog Communication            |           |        |          |                             | 71         | 23        |           | 94 P        |           | 25 P |             |         |   |  |
|                                 |           |        |          |                             |            |           |           |             |           |      | 682         |         | P |  |



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| SEAT No | P R No    | GENDER | Attempts | NAME OF CANDIDATE             | PAPER DESCRIPTION               | THEORY | SESSIONAL | TERM WORK | TOTAL  | PRACTICAL | ORAL | TOTAL   | REMARKS |   |
|---------|-----------|--------|----------|-------------------------------|---------------------------------|--------|-----------|-----------|--------|-----------|------|---------|---------|---|
| 231     | 201610577 | M      | 1        | KAVALEKAR ABHIJIT SHASHIKANT  | Digital Signal Processing       | 45     | 12        | 21        | 78 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 41     | 16        |           | 57 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 30     | 12        |           | 42 F   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 61     | 12        |           | 73 P   | 11 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 72     | 16        |           | 88 P   | 20 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 51     | 9         |           | 60 P   |           | 12 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 441     | F       |   |
| 232     | 201610580 | M      | 1        | KENI RAJ ALIAS VISHANT PRAMOD | Digital Signal Processing       | 48     | 15        | 22        | 85 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 50     | 13        |           | 63 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 47     | 20        |           | 67 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 68     | 21        |           | 89 P   | 24 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 56     | 17        |           | 73 P   | 20 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 46     | 14        |           | 60 P   |           | 22 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 503     | P       |   |
| 233     | 201610581 | F      | 1        | KENI RAVINA RAJIV             | Digital Signal Processing       | 49     | 15        | 24        | 88 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 50     | 18        |           | 68 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 40     | 14        |           | 54 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 65     | 15        |           | 80 P   | 18 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 64     | 14        |           | 78 P   | 24 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 60     | 20        |           | 80 P   |           | 20 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 510     | P       |   |
| 235     | 201610583 | M      | 1        | LOHANI MAYANK RAJESH          | Digital Signal Processing       | 47     | 19        | 22        | 88 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 47     | 15        |           | 62 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 48     | 18        |           | 66 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 58     | 16        |           | 74 P   | 12 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 50     | 14        |           | 64 P   | 22 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 36     | #4        | 21        | 57 P#4 |           | 17 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 462 #10 | P       |   |
| 236     | 201610752 | M      | 1        | MANVENDRA SINGH               | Digital Signal Processing       | 56     | 18        | 21        | 95 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 42     | 21        |           | 63 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 50     | 21        |           | 71 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 62     | 20        |           | 82 P   | 25 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 75     | 20        |           | 95 P   | 25 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 48     | 19        |           | 67 P   |           | 17 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 540     | P       |   |
| 240     | 201610589 | M      | 1        | NABAR VISMAY                  | Digital Signal Processing       | 45     | 18        | 22        | 85 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 60     | 25        |           | 85 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 70     | 23        |           | 93 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 74     | 19        |           | 93 P   | 25 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 63     | 25        |           | 88 P   | 25 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 64     | 23        |           | 87 P   |           | 24 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 605     | P       |   |
| 241     | 201705126 | F      | 1        | NAIK GANDHALI DATTARAM        | Digital Signal Processing       | 47     | 7         | 21        | 75 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 38     | \$2       | 10        | 48 P   | \$2       |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 25     | 15        |           | 40 F   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 64     | 12        |           | 76 P   | 10 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 76     | 10        |           | 86 P   | 16 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 57     | 12        |           | 69 P   |           | 16 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 436     | \$2     | F |
| 243     | 201610594 | F      | 1        | NAIK PALLAVI UMESH            | Digital Signal Processing       | 44     | 15        | 25        | 84 P   |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 79     | 20        |           | 99 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 64     | 23        |           | 87 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 80     | 23        |           | 103 P  | 25 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 78     | 20        |           | 98 P   | 25 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 70     | 24        |           | 94 P   |           | 25 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 640     | P       |   |
| 246     | 201610601 | M      | 1        | NAIK SUYOG ROHIDAS            | Digital Signal Processing       | 35     | \$5       | 11        | 70 P   | \$5       |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 57     | 17        | 24        | 74 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 46     | 18        |           | 64 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 56     | 17        |           | 73 P   | 19 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 50     | 12        |           | 62 P   | 20 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 55     | 19        |           | 74 P   |           | 18 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 474     | \$5     | P |
| 248     | 201610603 | F      | 1        | NIDHI RAJEEV NAIK             | Digital Signal Processing       | 67     | 15        | 24        | 106 P  |           |      |         |         |   |
|         |           |        |          |                               | Transmission Lines and Antennas | 62     | 21        |           | 83 P   |           |      |         |         |   |
|         |           |        |          |                               | Control System Engineering      | 54     | 22        |           | 76 P   |           |      |         |         |   |
|         |           |        |          |                               | Embedded Systems                | 64     | 16        |           | 80 P   | 13 P      |      |         |         |   |
|         |           |        |          |                               | VLSI Design and Technology      | 80     | 23        |           | 103 P  | 25 P      |      |         |         |   |
|         |           |        |          |                               | Analog Communication            | 58     | 20        |           | 78 P   |           | 22 P |         |         |   |
|         |           |        |          |                               |                                 |        |           |           |        |           |      | 586     | P       |   |



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| SEAT No | P R No    | GENDER | Attempts | NAME OF CANDIDATE                 | PAPER DESCRIPTION               | THEORY | SESSIONAL | TERM WORK | TOTAL | PRACTICAL | ORAL | TOTAL | REMARKS |   |
|---------|-----------|--------|----------|-----------------------------------|---------------------------------|--------|-----------|-----------|-------|-----------|------|-------|---------|---|
| 249     | 201610604 | M      | 1        | PAI G VIKRAM                      | Digital Signal Processing       | 39     | \$1 16    | 24        | 79 P  | \$1       |      |       |         |   |
|         |           |        |          |                                   | Transmission Lines and Antennas | 40     | 21        |           | 61 P  |           |      |       |         |   |
|         |           |        |          |                                   | Control System Engineering      | 41     | 17        |           | 58 P  |           |      |       |         |   |
|         |           |        |          |                                   | Embedded Systems                | 65     | 16        |           | 81 P  | 20 P      |      |       |         |   |
|         |           |        |          |                                   | VLSI Design and Technology      | 60     | 18        |           | 78 P  | 24 P      |      |       |         |   |
|         |           |        |          |                                   | Analog Communication            | 53     | 18        |           | 71 P  |           | 18 P |       |         |   |
|         |           |        |          |                                   |                                 |        |           |           |       |           |      | 490   | \$1     | P |
| 250     | 201610959 | F      | 1        | PALAN DIKSHA YOGESH               | Digital Signal Processing       | 49     | 10        | 22        | 81 P  |           |      |       |         |   |
|         |           |        |          |                                   | Transmission Lines and Antennas | 35     | \$5 23    |           | 58 P  | \$5       |      |       |         |   |
|         |           |        |          |                                   | Control System Engineering      | 38     | \$2 10    |           | 48 P  | \$2       |      |       |         |   |
|         |           |        |          |                                   | Embedded Systems                | 47     | 11        |           | 58 P  | 17 P      |      |       |         |   |
|         |           |        |          |                                   | VLSI Design and Technology      | 65     | 14        |           | 79 P  | 23 P      |      |       |         |   |
|         |           |        |          |                                   | Analog Communication            | 47     | 20        |           | 67 P  |           | 16 P |       |         |   |
|         |           |        |          |                                   |                                 |        |           |           |       |           |      | 447   | \$7     | P |
| 253     | 201704561 | F      | 1        | POLEM REDDY SONALI VENKATA RAMANA | Digital Signal Processing       | 47     | 10        | 24        | 81 P  |           |      |       |         |   |
|         |           |        |          |                                   | Transmission Lines and Antennas | 38     | \$2 16    |           | 54 P  | \$2       |      |       |         |   |
|         |           |        |          |                                   | Control System Engineering      | 22     | 17        |           | 39 F  |           |      |       |         |   |
|         |           |        |          |                                   | Embedded Systems                | 55     | 16        |           | 71 P  | 12 P      |      |       |         |   |
|         |           |        |          |                                   | VLSI Design and Technology      | 60     | 16        |           | 76 P  | 16 P      |      |       |         |   |
|         |           |        |          |                                   | Analog Communication            | 41     | 18        |           | 59 P  |           | 15 P |       |         |   |
|         |           |        |          |                                   |                                 |        |           |           |       |           |      | 423   | \$2     | F |
| 255     | 201610755 | M      | 1        | PRATIK SINGH NEGI                 | Digital Signal Processing       | 35     | \$5 11    | 21        | 67 P  | \$5       |      |       |         |   |
|         |           |        |          |                                   | Transmission Lines and Antennas | 26     | 13        |           | 39 F  |           |      |       |         |   |
|         |           |        |          |                                   | Control System Engineering      | 24     | 14        |           | 38 F  |           |      |       |         |   |
|         |           |        |          |                                   | Embedded Systems                | 41     | 13        |           | 54 P  | 8 F       |      |       |         |   |
|         |           |        |          |                                   | VLSI Design and Technology      | 66     | 13        |           | 79 P  | 17 P      |      |       |         |   |
|         |           |        |          |                                   | Analog Communication            | 52     | 12        |           | 64 P  |           | 15 P |       |         |   |
|         |           |        |          |                                   |                                 |        |           |           |       |           |      | 381   | \$5     | F |
| 258     | 201610625 | M      | 1        | SAMANT KANISHK SHAMSUNDAR         | Digital Signal Processing       | 75     | 20        | 25        | 120 P |           |      |       |         |   |
|         |           |        |          |                                   | Transmission Lines and Antennas | 80     | 21        |           | 101 P |           |      |       |         |   |
|         |           |        |          |                                   | Control System Engineering      | 84     | 19        |           | 103 P |           |      |       |         |   |
|         |           |        |          |                                   | Embedded Systems                | 77     | 21        |           | 98 P  | 25 P      |      |       |         |   |
|         |           |        |          |                                   | VLSI Design and Technology      | 88     | 25        |           | 113 P | 25 P      |      |       |         |   |
|         |           |        |          |                                   | Analog Communication            | 76     | 24        |           | 100 P |           | 24 P |       |         |   |
|         |           |        |          |                                   |                                 |        |           |           |       |           |      | 709   |         | P |
| 259     | 201610615 | F      | 1        | SAWAIKAR ESHA NARENDRA            | Digital Signal Processing       | 40     | 10        | 22        | 72 P  |           |      |       |         |   |
|         |           |        |          |                                   | Transmission Lines and Antennas | 41     | 18        |           | 59 P  |           |      |       |         |   |
|         |           |        |          |                                   | Control System Engineering      | 44     | 16        |           | 60 P  |           |      |       |         |   |
|         |           |        |          |                                   | Embedded Systems                | 67     | 15        |           | 82 P  | 10 P      |      |       |         |   |
|         |           |        |          |                                   | VLSI Design and Technology      | 73     | 17        |           | 90 P  | 20 P      |      |       |         |   |
|         |           |        |          |                                   | Analog Communication            | 37     | \$3 18    |           | 55 P  | \$3       | 18 P |       |         |   |
|         |           |        |          |                                   |                                 |        |           |           |       |           |      | 466   | \$3     | P |
| 270     | 201610637 | F      | 1        | VELIP KALPITA B .                 | Digital Signal Processing       | 35     | \$5 10    | 21        | 66 P  | \$5       |      |       |         |   |
|         |           |        |          |                                   | Transmission Lines and Antennas | 35     | \$5 12    |           | 47 P  | \$5       |      |       |         |   |
|         |           |        |          |                                   | Control System Engineering      | 31     | 13        |           | 44 F  |           |      |       |         |   |
|         |           |        |          |                                   | Embedded Systems                | 60     | 14        |           | 74 P  | 11 P      |      |       |         |   |
|         |           |        |          |                                   | VLSI Design and Technology      | 55     | 15        |           | 70 P  | 18 P      |      |       |         |   |
|         |           |        |          |                                   | Analog Communication            | 50     | 19        |           | 69 P  |           | 12 P |       |         |   |
|         |           |        |          |                                   |                                 |        |           |           |       |           |      | 411   | \$10    | F |

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