Course: RC 2016-17 REVALUATION RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER

PAPER DESCRIPTION	NAME OF CANDIDATE THEORY	Y SE	SSIONAL	TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	REMA	ARKS
203 201610774 M 1	ABHISHEK BARGE						-			
igital Signal Processing	56	•	10	22	88 P					
ransmission Lines and Antennas	50		15		65 P					
Control System Engineering	55		12		67 P					
imbedded Systems	63		12		75 P	13 P				
/LSI Design and Technology	65 51		17 15		82 P 66 P	24 P	18 P			
analog Communication	51		15		00 P		10 P	400		
204 201610652 M 1	AIGAL ABHAY CHAND	RAKANT	-					498		<u>P</u>
Digital Signal Processing	43			22	76 P					
ransmission Lines and Antennas	40		14		54 P					
Control System Engineering	42		14		56 P					
Embedded Systems	59		11		70 P	10 P				
/LSI Design and Technology	62		21		83 P	20 P				
Analog Communication	29		12		41 F		17 P			
205 201704559 F 1	AKALWADI ASHVINI S							427		F
Digital Signal Processing	26			22	58 F					
ransmission Lines and Antennas	26 17		15	22	30 F					
Control System Engineering	24		15		32 F					
Embedded Systems	55		15		70 P	3 F				
/LSI Design and Technology	52		15		67 P	16 P				
Analog Communication	38	\$2 <i>^</i>	10		48 P	\$2	13 P			
								346	\$2	F
207 201610536 F 1	ATHALYE JANHAVI SH									
Digital Signal Processing	67			23	104 P					
Fransmission Lines and Antennas	40		21		61 P	Φ.5				
Control System Engineering			14 10		39 P#10					
Embedded Systems	65 70		18		83 P	19 P				
/LSI Design and Technology Analog Communication	70 41		21 17		91 P 58 P	20 P	19 P			
maiog communication								494 #10	n \$5	— Р
213 201610548 M 1	CHODANKAR NIHAL D	DEVIDAS						757 #10	υ ψυ	
Digital Signal Processing	40			20	71 P					
Fransmission Lines and Antennas	35	\$5 2	20		55 P	\$5				
Control System Engineering	44		19		63 P					
Embedded Systems	43		11		54 P	18 P				
VLSI Design and Technology Analog Communication	64 35		11 18		75 P 53 P	24 P \$5	18 P			
analog Communication		<u> </u>	10		33 F	ΨΟ	10 F	404	040	
218 201610554 M 1	DESSAI DIGVIJAY MA	RUTI						431	\$10	Р
Digital Signal Processing	30		11	22	63 F					
Fransmission Lines and Antennas	35		16	22		\$5				
Control System Engineering	40		13		53 P	Ψ0				
Embedded Systems	51		6		57 P	11 P				
VLSI Design and Technology	52		10		62 P	18 P				
Analog Communication	51		10		61 P		21 P			
								397	\$5	F
222 201704558 F 1	EKAWADE PRANALEE			21	71 P					
Digital Signal Processing	40	•	10	21	71 P 34 F					
Digital Signal Processing Transmission Lines and Antennas				21	71 P 34 F 39 F					
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering	40 22		10 12	21	34 F	8 F				
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology	40 22 21 42 60		10 12 18 10	21	34 F 39 F 52 P 70 P	8 F 20 P				
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology	40 22 21 42		10 12 18 10	21	34 F 39 F 52 P		13 P			
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication	40 22 21 42 60 53		10 12 18 10	21	34 F 39 F 52 P 70 P		13 P	370		F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication	40 22 21 42 60 53 GAONKAR DEEPTI DE	HARMA	10 12 18 10 10		34 F 39 F 52 P 70 P 63 P		13 P			F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems VLSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing	40 22 21 42 60 53 GAONKAR DEEPTI DE	HARMA	10 12 18 10 10 10	20	34 F 39 F 52 P 70 P 63 P		13 P			F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54	HARMA	10 12 18 10 10 10 10		34 F 39 F 52 P 70 P 63 P 88 P 74 P		13 P			F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27	HARMA	10 12 18 10 10 10 10		34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F	20 P	13 P			F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60	HARMA	10 12 18 10 10 10 10 13 20 14		34 F 39 F 52 P 70 P 63 P 88 P 74 P	20 P	13 P			F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27	HARMA	10 12 18 10 10 10 10		34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P	20 P	13 P			F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51	HARMA	10 12 18 10 10 10 10 13 20 14 11		34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P	20 P				
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51	HARMA	10 12 18 10 10 10 10 13 20 14 11 15 16	20	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P	20 P		370		
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51	HARMA	10 12 18 10 10 10 10 13 20 14 11 15 16		34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P	20 P		370		F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems //LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems //LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Fransmission Lines and Antennas	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51	HARMA 2	10 12 18 10 10 10 11 13 20 14 11 15 16	20	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P	20 P		370		
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Fransmission Lines and Antennas Control System Engineering	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40	HARMA	10 12 18 10 10 10 11 13 20 14 11 15 16	20	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P	20 P 10 P 20 P		370		
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems Control System Engineering Fransmission Lines and Antennas Control System Engineering Embedded Systems	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40 62	HARMA Z	10 12 18 10 10 10 110 13 20 14 11 15 16	20	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P 85 P 85 P 85 P 80 P	20 P 10 P 20 P		370		
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Embedded Systems /LSI Design and Technology	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40	HARMA Z	10 12 18 10 10 10 11 13 20 14 11 15 16	20	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P	20 P 10 P 20 P		370		
Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Embedded Systems /LSI Design and Technology	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40 62 68	HARMA Z	10 12 18 10 10 10 11 13 20 14 11 15 16	20	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P 85 P 85 P 59 P 80 P 87 P	20 P 10 P 20 P	18 P	370		F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 230 201610574 F 1	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40 62 68 51	HARMA	10 12 18 10 10 10 110 13 20 14 11 15 16 11 20 19 18 19 18	25	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P 85 P 85 P 85 P 89 P 80 P 87 P 69 P	20 P 10 P 20 P	18 P	370 477		F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 230 201610574 F 1 Digital Signal Processing	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40 62 68 51	HARMA TANO IANDKISI	10 12 18 10 10 10 10 13 20 14 11 15 16 11 18 19 18 19 18	25	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P 85 P 85 P 85 P 89 P 80 P 87 P 69 P	20 P 10 P 20 P	18 P	370 477		F
Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 230 201610574 F 1 Digital Signal Processing Fransmission Lines and Antennas 230 201610574 F 1 Digital Signal Processing Fransmission Lines and Antennas	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40 62 68 51	HARMA ANO IANDKISI	10 12 18 10 10 10 110 13 20 14 11 15 16 11 15 16 19 18 19 18	25	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P 85 P 85 P 85 P 89 P 89 P 81 P 97 P	20 P 10 P 20 P	18 P	370 477		F
Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 230 201610574 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering 230 201610574 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering 250 201610574 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40 62 68 51 KANEKAR SHWETA N 66 78 70	HARMA ANO IANDKISH	10 12 18 10 10 10 110 13 20 14 11 15 16 11 15 16 19 18 19 18	25	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P 85 P 85 P 85 P 89 P 80 P 87 P 69 P	20 P 10 P 20 P	18 P	370 477		F
Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 225 201610566 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 226 201610691 F 1 Digital Signal Processing Transmission Lines and Antennas Control System Engineering Embedded Systems /LSI Design and Technology Analog Communication 230 201610574 F 1 Digital Signal Processing Transmission Lines and Antennas 230 201610574 F 1 Digital Signal Processing Transmission Lines and Antennas	40 22 21 42 60 53 GAONKAR DEEPTI DH 55 54 27 60 73 51 GAONKAR MANJITA T 49 65 40 62 68 51	HARMA ANO ANDKISI	10 12 18 10 10 10 110 13 20 14 11 15 16 11 18 19 18 19 18	25	34 F 39 F 52 P 70 P 63 P 88 P 74 P 41 F 71 P 88 P 67 P 85 P 85 P 85 P 89 P 89 P 81 P 97 P	20 P 10 P 20 P	18 P	370 477		

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Course: RC 2016-17 REVALUATION RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER

SEAT No P R No GENDER Attempts PAPER DESCRIPTION	NAME OF CANDIDATE THEORY	SESSION	AL TERM WORK	TOTAL	PRACTICAL	ORAL	TOTAL	REMA	ΔDV
231 201610577 M 1	KAVALEKAR ABHIJIT SH		AL TERM WORK	TOTAL	PRACTICAL	URAL	TOTAL	KEIVIA	ARK
Digital Signal Processing	45	12	21	78 P					
ransmission Lines and Antennas	41	16		57 P					
Control System Engineering	30	12		42 F					
Embedded Systems	61	12		73 P	11 P				
/LSI Design and Technology	72	16		88 P	20 P				
Analog Communication	51	9		60 P		12 P			
							441		F
232 201610580 M 1 Digital Signal Processing	KENI RAJ ALIAS VISHAN 48	<u>11 PRAMOL</u> 15	22	85 P					
ransmission Lines and Antennas	50	13		63 P					
Control System Engineering	47	20		67 P					
Embedded Systems	68	21		89 P	24 P				
LSI Design and Technology	56	17		73 P	20 P				
analog Communication	46	14		60 P		22 P			
							503		F
233 201610581 F 1	KENI RAVINA RAJIV	4.5	0.4						
Pigital Signal Processing	49	15	24	88 P					
ransmission Lines and Antennas	50 40	18 14		68 P 54 P					
Control System Engineering Embedded Systems	65	15		54 P 80 P	18 P				
LSI Design and Technology	64	14		78 P	24 P				
nalog Communication	60	20		80 P	_ · · ·	20 P			
							510		F
235 201610583 M 1	LOHANI MAYANK RAJES								
Digital Signal Processing	47	19	22	88 P					
ransmission Lines and Antennas	47	15		62 P					
Control System Engineering	48 50	18 16		66 P	40 D				
mbedded Systems LSI Design and Technology	58 50	16 14		74 P 64 P	12 P 22 P				
nalog Communication	36 #4			64 P 57 P#4	22 F	17 P			
<u> </u>	33 111						462 #10		F
236 201610752 M 1	MANVENDRA SINGH						7 0∠ #10		
Digital Signal Processing	56	18	21	95 P					
ransmission Lines and Antennas	42	21		63 P					
Control System Engineering	50	21		71 P					
mbedded Systems	62	20		82 P	25 P				
/LSI Design and Technology Analog Communication	75 48	20 19		95 P 67 P	25 P	17 P			
mang communication				07.1			540		F
240 201610589 M 1	NABAR VISMAY						340		
Digital Signal Processing	45	18	22	85 P					
ransmission Lines and Antennas	60	25		85 P					
Control System Engineering	70	23		93 P					
Embedded Systems	74	19		93 P	25 P				
/LSI Design and Technology	63	25		88 P	25 P	24 D			
analog Communication	64	23		87 P		24 P			
	NAIK GANDHALI DATTAI	DAM					605		F
Digital Signal Processing	47	7	21	75 P					
ransmission Lines and Antennas	38	\$2 10			52				
Control System Engineering	25	15		40 F					
Embedded Systems	64	12		76 P	10 P				
/LSI Design and Technology	76	10		86 P	16 P				
nalog Communication	57	12		69 P		16 P			
001010501 -	NAUZ BALL MELLIN						436	\$2	F
243 201610594 F 1	NAIK PALLAVI UMESH 44	15	25	84 P					_
igital Signal Processing ransmission Lines and Antennas	79	20	۷)	84 P 99 P					
Control System Engineering	64	23		99 F 87 P					
Embedded Systems	80	23		103 P	25 P				
LSI Design and Technology	78	20		98 P	25 P				
nalog Communication	70	24		94 P		25 P			
							640		
246 201610601 M 1	NAIK SUYOG ROHIDAS		0.4	70.5	\				
ransmission Lines and Antonnas	35 57	\$5 11	24	70 P \$ 74 P	55				
ransmission Lines and Antennas	57 46	17 18		74 P 64 P					
ontrol System Engineering mbedded Systems	46 56	18 17		64 P 73 P	19 P				
LSI Design and Technology	50	17		62 P	20 P				
	55	19		74 P	_~ .	18 P			
nalog Communication							474	\$5	ı
nalog Communication	NIDHI RAJEEV NAIK								
248 201610603 F 1		15	24	106 P					
igital Signal Processing	67								
248 201610603 F 1 Digital Signal Processing ransmission Lines and Antennas	62	21		83 P					
248 201610603 F 1 Digital Signal Processing Fransmission Lines and Antennas Control System Engineering	62 54	21 22		76 P	40 D				
248 201610603 F 1 igital Signal Processing ransmission Lines and Antennas ontrol System Engineering mbedded Systems	62 54 64	21 22 16		76 P 80 P	13 P				
48 201610603 F 1 gital Signal Processing ansmission Lines and Antennas ontrol System Engineering	62 54	21 22		76 P	13 P 25 P	22 P			

REVALUATION RESULT REGISTER FOR B.E EXAMINATION HELD IN ELECTRONICS & TELECOMMUNICATION ENGINEERING FOR 5th SEMESTER

ateau, Goa 403 206 Course : RC 2016-17

EXAM

COLLEGE: GOA COLLEGE OF ENGINEERING SEAT No PR No GENDER Attempts NAME OF CANDIDATE PAPER DESCRIPTION SESSIONAL TERM WORK TOTAL **PRACTICAL** ORAL TOTAL **REMARKS THEORY** 201610604 1249 Μ PAI G VIKRAM Digital Signal Processing 39 \$1 16 24 79 P \$1 Transmission Lines and Antennas 40 21 61 P Control System Engineering 58 P 41 17 **Embedded Systems** 65 16 81 P 20 P VLSI Design and Technology 60 18 78 P 24 P **Analog Communication** 53 18 71 P 18 P Ρ 490 \$1 1250 201610959 PALAN DIKSHA YOGESH Digital Signal Processing 10 22 81 P 49 Transmission Lines and Antennas 35 \$5 23 58 P \$5 Control System Engineering 38 \$2 10 48 P \$2 **Embedded Systems** 47 11 58 P 17 P VLSI Design and Technology 65 14 79 P 23 P 16 P **Analog Communication** 47 20 67 P 447 \$7 Ρ 201704561 POLEM REDDY SONALI VENKATA RAMANA Digital Signal Processing 81 P 47 10 Transmission Lines and Antennas 38 16 54 P \$2 \$2 Control System Engineering 22 17 39 F 12 P **Embedded Systems** 55 16 71 P VLSI Design and Technology 60 76 P 16 P 16 **Analog Communication** 41 15 P 18 59 P 423 \$2 F 201610755 255 PRATIK SINGH NEGI Digital Signal Processing \$5 11 21 67 P \$5 Transmission Lines and Antennas 26 13 39 F 24 Control System Engineering 14 38 F **Embedded Systems** 41 8 F 13 54 P VLSI Design and Technology 66 13 79 P 17 P **Analog Communication** 15 P 52 12 64 P 381 \$5 F 258 201610625 SAMANT KANISHK SHAMSUNDAR Μ Digital Signal Processing 20 25 120 P 75 Transmission Lines and Antennas 80 21 101 P Control System Engineering 84 19 103 P **Embedded Systems** 77 21 25 P 98 P VLSI Design and Technology 88 25 113 P 25 P Analog Communication 76 24 100 P 24 P 709 Ρ 201610615 259 SAWAIKAR ESHA NARENDRA Digital Signal Processing 40 10 22 72 P Transmission Lines and Antennas 41 18 59 P Control System Engineering 44 16 60 P **Embedded Systems** 67 15 82 P 10 P VLSI Design and Technology 73 17 90 P 20 P **Analog Communication** 37 55 P 18 P \$3 18 \$3 466 \$3 Ρ 1270 201610637 VELIP KALPITA B Digital Signal Processing 35 \$5 10 21 66 P \$5 Transmission Lines and Antennas 35 \$5 12 47 P \$5 Control System Engineering 31 44 F 13 **Embedded Systems** 60 14 74 P 11 P VLSI Design and Technology 55 15 70 P 18 P **Analog Communication** 50 69 P 12 P 19

Read By :

Checked By :

Date

Assistant Registrar-E(Proff.)

Controller Of Examinations

Registrar

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